Ali Ameri

Address: 1825 Francisco st. Apt. E Berkeley, California. Cell phone: (+1) 510-697-8161 E-mail: ameriali@gmail.com

EDUCATION

•	Ph.D., Electrical Engineering, IC Design
	University of California Berkeley. (Sept. 2015 – Present)

- Master of Engineering (Thesis-based), IC Design McGill University. Graduation: October 2011
- Bachelor of Science, Electrical Engineering Electronics • University of Tehran. Graduation: July 2008

EXPERIENCE

Design Engineer

Granite SemiCom Inc., Manager: Dr. Ken Martin

- Behavioral modeling of digital PLLs for precision clock generation and jitter cleaning applications. •
- Design and layout of various PLL building blocks in TSMC 28HPM, 40LP, and 40G.
- Test board design and measurement of the PLL. •
- Design of a sensor interface board and data acquisition system for industrial pressure sensors. •

Research Assistant

University of California Berkeley, Advisor: Prof. Ali M. Niknejad

Flow cytometry sensor design for single biological cell identification and characterization. •

Research Assistant

McGill University, Advisor: Prof. Gordon W. Roberts

Design of a time-mode low-pass IIR filter in 0.13um IBM CMOS technology as part of my Master's • thesis.

Teaching Assistant

ECE Department, McGill University:

- Electrical Measurement Laboratory
- Mixed-Signal IC Test and Measurement
- ECE Department, University of Tehran
- **Electronics Circuits II Laboratory** •
- **Electronics Circuits I Laboratory**

PUBLICATION

• A. Ameri, G.W. Roberts, "Time-Mode Reconstruction IIR Filters for $\Sigma\Delta$ Phase Modulation Applications," ACM Great Lakes Symposium on VLSI 2011, pp. 423-426.

SKILLS

Software:

- Circuit and layout design platforms: Cadence, Laker, ADS. •
- Circuit simulators: SPECTRE, HSPICE, SMASH. •
- System level verification and programming: Simulink/MATLAB.
- Scripting languages: Python, TCL.

Sept. 2008 – June 2011

Sept. 2015 – Present

Winter 2011, Fall 2010 Winter 2010

> Winter 2008 **Fall 2007**

(GPA: 4.0/4.0)

(GPA: 17.15/20)

Sept. 2011-Aug. 2015

- Verilog simulators: Modelsim, VCS.
- Printed Circuit Board (PCB) design tool: Altium Designer, Design Spark.
- Operating Systems: Linux (Ubuntu, RedHat, CentOS), Windows.

Communication:

• Instructed tutorial sessions and delivered short lectures.

HONORS

- Principal's Graduate Fellowship (PGF), ECE Department, McGill University.
- Ranked 4th among Electronics Engineering students, ECE Department, University of Tehran.