Hesham Ahmed Amin Beshary

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Research Interests	mm-Wave, Phased-Arrays, Beamformers, Radar Systems, Transceivers, PAs, Power Management, IoT, ADCs, CDRs and Software Engineering	
Education		
(2020–Present)	 PhD: University of California at Berkeley Department of Electrical Engineering and Computer Science Major: Integrated Circuits Research area: Squint-free sub-THz massive MIMO phased arrays Advisor: Prof. Ali M. Niknejad GPA: 4.0/4.0 	
(2017–2020)	 MSc: Cairo University, Cairo, Egypt Department of Electronics and Electrical Communications Thesis: Phase Shifters and Power Splitters Design and Implementation Techniques for mm-Wave Beamforming Applications. GPA: 4.0/4.0 	
(2012–2017)	 BSc, Distinction with Honor: Cairo University, Cairo, Egypt Department of Electronics and Electrical Communications Rank: 1st/240 Cumulative rate: 96.04% with equivalent GPA: 4.0/4.0 Graduation project: Design and implementation of a fully integrated 28 GHz 4-channel phased-array transceiver for 5G applications in 65-nm CMOS technology, sponsored by Analog Devices Inc, Egypt. 	
Patents		
(2019)	M. Abd-elsalam, Hesham Beshary, M cluding RC-LR sections." US Patent	M. A. Abdalla, "Polyphase filter (PPF) in- Number 11,101,782 B1
PUBLICATIONS		
(2022)	Lorenzo Iotti, Ali Niknejad, "A Low- Four-Channel Receiver with Integrate	ham Beshary, Meng Wei, Emily Naviasky, Power and Energy-Efficient D-Band CMOS ed LO Generation for Digital Beamforming European Solid State Circuits Conference
(2022)		Chou, Hesham Beshary, Sashank Krishna- D-Band Packaged CMOS Integrated Trans- ASSCC 2022 (Accepted)

(2020)	H. Beshary et al., "A Compact Low-Loss On-chip N-Way Wilkinson Power Divider for mm-Wave 5G Applications," 2020 IEEE 63rd International Midwest Symposium on Circuits and Systems (MWSCAS), Springfield, MA, USA, 2020		
(2019)	H. A. Ameen et al., "A 28 GHz four-channel phased-array transceiver in 65-nm CMOS technology for 5G applications," <i>AEU - International Journal of Electronics and Communications 98</i> (2019)		
(2017)	H. A. Ameen et al., "A 28 GHz Four-Channel Phased-Array transceiver in 65-nm CMOS technology for 5G applications," in 2017 29th International Conference on Microelectronics (ICM) (IEEE-ICM2017), Crowne Plaza Hotel, Beirut, Lebanon, Dec. 2017.		
Work Experience			
(Aug 2020–Present)	Graduate Student Researcher at Berkeley Wireless Research Center (BWRC). Focusing on the design of mm-wave and sub-THz circuits and systems.		
(Sep 2017–Aug 2020)		vices, Egypt. Design and implementation 5G beamformers and 5G UDCs including: RF switches Power detectors Amplifiers outs, including the following products: ADMV4828 ADMV1139	
(Sep 2017–Aug 2020)	 Teaching/Research Assistant at Department of Electronics and Electrical Communications, Cairo University. Supervising many undergraduate workshops. Teaching in tutorials the following courses: ELC201A (Electronics): [Fall 2019] Frequency response - Feedback amplifiers - Oscillators - Noise analysis ELC206 (Signal Analysis): [Spring 2019]/[Spring 2018] CT and DT signals and systems - Fourier series - Fourier transform - Z-transform - Sampling ELC401A (Electronics): [Fall 2018] PLLs - Op-amps design and stability - Memory design and interface. [Fall 2018] ELC307A (Systems): [Fall 2018] System representation - Transfer function - Block diagram - Impulse response - Routh-Hurwitz stability - State space representation - Observability - Controllability. ELCN201 (Electronics): [Fall 2017] Single stage amplifiers - Frequency response - Current mirrors. 		
(Sep 2016–Aug 2017)	Graduation project work. System-level design for a beamformer operating at 28 GHz in 65-nm CMOS technology and determining the sub-blocks' specifications, designing of an active phase-shifter and handling the system-level integration and simulations. The circuit layout is extracted by EM simulation (Sonnet).		

(July 2016–Sep 2016)	Internship at Fritz Huettinger Chair of Microelectronics, IMTEK, University of Freiburg, Germany. Design and implementation of standard cells for logic gates, flip-flops and subthreshold circuits at ultra-low voltage supply for IoT applications and Verilog-A programming.		
(July 2015–Sep 2015)	Full-time job at Security Meter, Egypt. UHCI and EHCI USB drivers development for OS and hobby-kernels.		
(Feb 2015–May 2015)	Educational teamwork project. Design and implementation of pipelined microprocessor using VHDL.		
(Aug 2014–Mar 2015)	Internship at Inmobily, Egypt. Open source, Intel baremetal programming and OS development.		
Technical Skills	RF and mm-wave circuits designAnalog designDigital design and VLSI designMicrowave designC/C++ programmingLATEXVery good knowledge of:IATEXI Cadence tools and ADSEM solvers such as EMX, Sonnet, Momentum, EMPro and HFSSI MATLAB, SimulinkModelsim		
Soft Skills	Design sense and abstractive thinking Work-life balance Communication skills	Leadership Resilience	
Languages	Arabic, Native. English, Proficient.	German, Beginner.	
Hobbies	Reading, self-studying and keeping track of modern technologies.		
References	 Prof. Ali Niknejad Electrical Engineering and Computer Sciences, UC Berkeley (PhD Advisor) Email: niknejad@berkeley.edu Dr. Ahmed Ibrahim Khalil, Ph.D., North Carolina State University ADI Fellow and General Manager at Analog Devices Egypt Email: ahmed.khalil@analog.com Prof. Ahmed N. Mohieldin, Ph.D., University of Texas A&M Department of Electronics and Electrical Communications Engineering, Cairo University, Egypt. (MS supervisor) Email: anader@eng1.cu.edu.eg 		
	 Prof. Mohamed A. Y. Abdalla, <i>Ph.D.</i>, University of Toronto Department of Electronics and Electrical Communications Engineering, Cairo University, Egypt. (MS supervisor) Email: youssef@ieee.org 		